

ABSTRACT OF THE DISCLOSURE

It is an object of the present invention to provide a display panel driver with which power consumption can be reduced. The display panel driver includes a pixel data pulse generation circuit which generates pixel data pulses by connecting the column electrodes and a power source line in accordance with the pixel data to apply the pixel data pulses to the column electrodes, and a resonance pulse power circuit which generates a resonance pulse power source voltage to apply the resonance pulse power source voltage to the power source line, the resonance pulse power circuit changing the resonance amplitude of the resonance pulse power source voltage while keeping a maximum voltage of the resonance pulse power source voltage in accordance with a pattern of a pulse sequence of the pixel data pulses. A predicted power consumption of the resonance pulse power circuit is determined based on the pixel data for one field, and the pixel data pulse generation circuit is controlled so as to adjust the power consumption of the resonance pulse power circuit in accordance with the predicted power consumption.